

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:	Yeo, <i>et al.</i>	Docket No.:	TSM03-0847
Serial No.:	10/826,602	Art Unit:	1795
Filed:	April 16, 2004	Examiner:	Raymond, Brittany L.
For:	Lithography Apparatus for Manufacture of Integrated Circuits		

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT

Dear Sir:

The following amendments and remarks are presented in response to the Examiner's Office Action mailed August 22, 2008. Please amend the above-referenced application as follows.